

## MRGI AT A GLANCE

Situated away from raucous sounds of Hyderabad city, MRGI epitomizes technocracy at its best. Being one of the largest campuses in Telangana, MRGI promises eventful students'- oriented learning. The founder chairman Sri.Ch. Malla Reddy Garu began the journey as an educationalist to form the bridge between society, industry and academics. He is an unassuming dynamic leader with a keen interest in changing trends of higher education and students' employability in India. The diverse and enormous growth of the group unravels his realistic vision to cater to the needs of students in all areas of education. Education at MRGI brings all round and holistic development in students.

## ABOUT MRCE

*Malla Reddy College of Engineering* formerly CM Engineering College has been established under the aegis of the Malla Reddy Group of Institutions, a majestic empire, founded by Sri. CH. Malla Reddy garu. He has been in the field of education for the last 23 years with the intention of spearheading quality education among children from the school level itself. It is the largest cluster of technical institutions in the state of Telangana, with the student strength of 20,000 and staff strength of 2000.

## ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering is currently the hottest destination for the dream of the state with highly qualified faculty and the state of art laboratories. The department aims a creating quality engineers with sound technical knowledge and awareness of the latest advancements in existing and emerging technologies. We have intake of 120 students. In our department laboratories like are well established with the latest software as well as hardware equipments.

Department is having experienced faculty members in different fields of ECE like signal processing, embedded Systems and VLSI Design. Every semester we conduct workshops and guest lectures in recent trends for students as well as Faculty members.

## ABOUT STTP

The main objective of the STTP is to help build an understanding of how to use and where to apply these concepts with examples.

## OBJECTIVE

- The objective of the STTP is to introduce fundamentals concepts of VLSI, Nano Electronics and IoT with its applications.
- This training program focuses on specific design techniques using VLSI Design Tools & Software. It also focuses on embedded system techniques for IoT applications
- VLSI and Embedded Systems Design Techniques are the main pillar of most of the present research, industrial and commercial activities in Electronics Engineering.

## COURSE CONTENT

- Introduction to VLSI Technology
- Role of VLSI in IOT, Remote accessing, Secure transmission Multi-layered Neural Networks
- HDL programming & FPGA
- Hands on HDL & FPGA
- Nano technology basics
- Challenges
- Introduction to Internet of Things, IoT Architecture
- Introduction to Sensors-Hands on Training
- M2M Communication Protocols
- Hand on training Session with IoT
- Micro and Nano Technology: New Trends
- Socially Relevant Real Time IoT Devices & Its Applications

**AICTE SPONSORED Six days STTP on**

## **“RECENT TRENDS IN VLSI NANOELECTRONICS FOR IoT APPLICATIONS”**

### REGISTRATION FORM

Full Name: .....

Designation: .....

Institution/ Company: .....

Address: .....

.....

Contact Numbers: .....

Mobile: .....

E-Mail: .....

Date:.....

### **DECLARATION:**

I declare that all the details furnished above are true to the best of my knowledge and I agree to abide by the rules and regulations governing the conduct of AICTE-

**Date:** **Signature of the Applicant**

**Place:**

### SPONSORSHIP CERTIFICATE:

Mr/Ms/Dr \_\_\_\_\_ is a faculty member / an employee of our Institution / Organization and is hereby sponsored by us. He/ She will be permitted to attend the course, if selected.

**Signature with seal of  
Head of the Institution/organization**

### **CHIEF PATRON:**

#### **Sri. Ch. Malla Reddy Garu**

Founder Chairman, MRGI, Minister of Labour, Employment, Women and Child Development, Government of Telangana.

### **PATRONS:**

**Sri.Ch. Mahender Reddy**, Secretary, MRGI

**Dr.Ch.Bhadra Reddy**, Chairman, MRGI

### **ORGANIZING CHAIR:**

**Dr. M.S Reddy**, Principal MRCE

### **CONVENOR:**

**Dr.J .L Divya Shivani**, Assoc. Prof of ECE & HoD

### **COORDINATOR:**

**Mr.M.Shiva Kumar**, Asst. Prof of ECE, MRCE

### **ORGANIZING COMMITTEE:**

**Dr.J.Gladson**, Professor of CSE & Dean (R & D)

**Dr. P. Ashok Kumar**, Assoc. Prof of ECE, MRCE

**Mr. P. Venkatapathi**, Assoc. Prof of ECE, MRCE

**Mrs. P. Anitha**, Asst. Prof of ECE, MRCE

**Mr. D. Rajesh**, Asst. Prof of ECE, MRCE

**Mr. P. Anil Kumar**, Asst. Prof of ECE, MRCE

**Mrs. B. Manjula**, Asst. Prof of ECE, MRCE

**Mr. D. Rajendra Prasad**, Asst. Prof of ECE, MRCE

**Mr. D. Thirupathi**, Asst. Prof of ECE, MRCE

**Mr. Veera Babu**, Asst. Prof of ECE, MRCE

**Mr. K. Rajeshwar**, Asst. Prof of ECE, MRCE

**Mr. G. Sanjeev**, Asst. Prof of ECE, MRCE

**Mrs. S. NagaJyothi**, Asst. Prof of ECE, MRCE

**Mr. Ch. Moses**, Asst. Prof of ECE, MRCE

**Ms. K. Aksa Rani**, Asst. Prof of ECE, MRCE

### **RESOURCE PERSONS**

1. Dr. M. Asha Rani, Professor of ECE, JNTUH
2. Dr. Prakash Kodali, Asst. Prof of ECE, NIT, Warangal
3. Mr. Rithik Raj, IoT, Corporate Trainer, Mumbai

### **ELIGIBILITY**

Faculty and Researchers from AICTE approved Engineering Colleges, Research Scholars and participants from Industries and R&D organization are also eligible to attend the program

### **REGISTRATION**

#### **No Registration fees**

All the participants are kindly requested to register for this STTP through the **following link**

### **HOW TO APPLY**

#### **Fill google form:**

<https://forms.gle/E9WA4kQUVcEU5dGE8>

#### **Click & Join the WhatsApp Group**

<https://chat.whatsapp.com/LQFJ8Qy3LwDBpkVN4sk4V6>

### **IMPORTANT DATES & TIME**

Last date for receipt of Registration Form:  
17.03.21 @3.30 PM

#### **Session Time: (OFF LINE)**

**10.00am to 01.00pm (FN)**

**02.00pm to 04.00pm (AN)**

### **ADDRESS FOR COMMUNICATION**

AICTE Sponsored Six days STTP on

**“RECENT TRENDS IN VLSI  
NANO ELECTRONICS FOR IoT  
APPLICATIONS”**

Department of ECE

Malla Reddy College of Engineering,  
Near Kompally, Secundrabad-500014.

**PH:9908877459, 8374891166, 9843673957,**

**E-Mail: principal@mrce.in, hodece@mrce.in**



**AICTE SPONSORED  
SIX DAYS  
SHORT TERM TRAINING PROGRAM**

**(STTP)**

*on*

**“RECENT TRENDS IN VLSI  
NANO ELECTRONICS FOR IoT  
APPLICATIONS”**

**22.3.2021 & 27.03.2021**

*Organized by*

**DEPARTMENT OF ELECTRONICS &  
COMMUNICATION ENGINEERING**



**MALLA REDDY COLLEGE OF  
ENGINEERING**

**(Approved by AICTE – New Delhi, Affiliated to  
JNTU - Hyderabad, Accredited by NBA &  
Accredited by NAAC)**

**Maisammaguda, Dhulapally, (Post via Hakeempet)  
Near Kompally, Secundrabad-500014.**

